

ANNA UNIVERSITY OF TECHNOLOGY, COIMBATORE
B.E. / B.TECH. DEGREE EXAMINATIONS : NOV / DEC 2010
REGULATIONS : 2008
FIFTH SEMESTER : ECE
080290030 - MICROPROCESSORS AND APPLICATIONS

TIME : 3 Hours

Max. Marks : 100

PART - A

(20 x 2 = 40 MARKS)

ANSWER ALL QUESTIONS

1. Mention the classification of 8085 instruction set.
2. List out the limitations of 8085 can qualify as an MPU.
3. What are the general purpose registers used in 8085?
4. Mention the flag registers used in 8085.
5. What are the functions of BIU?
6. What is software interrupt?
7. List out the partitions of memory segments.
8. How the operating mode of 8086 is determined?
9. What is register addressing mode?
10. Mention the flag transfer instructions in 8086.
11. How the program execution transfer instructions are used in 8086?
12. Give the assembly language program statement format.
13. State the internal addresses for 8255A.
14. What is 8251A?
15. How the 8279 was initialized?
16. What is maximum clock frequency and read-back feature of 8254?
17. Mention the three standard device organizations of 64 KB density SRAM.
18. What is refreshing in DRAM ?

19. How to buffering the outputs of port devices?
20. What is shaft encoder?

PART - B

(5 x 12 = 60 MARKS)

ANSWER ANY FIVE QUESTIONS

21. Explain the 8085 internal architecture with neat block diagram
22. Describe the memory segmentation used in 8086 with neat diagram
23. With the help of diagram, explain the Maximum mode CPU module.
24. Explain the various types of instructions used in 8086 with suitable examples.
25. Describe the Programmable Peripheral Interface with neat diagram
26. With the help of diagram, describe the PIC 8259 architecture and function.
27. Explain the SRAM interfacing with 8086 with neat diagram
28. Describe the DMA interfacing with suitable diagram.

*****THE END*****