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**Question Paper Code : 50272**

B.E./B.Tech. DEGREE EXAMINATION, MAY/JUNE 2012.

Third Semester

Computer Science and Engineering

CS 1203/CS 1252 — SYSTEM SOFTWARE

(Common to Information Technology — Third/Seventh Semester)

(Regulation 2004)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. Define System software.
2. Write the instruction set of SIC machine.
3. What is meant by relocation?
4. Define Literals. Give an example.
5. Distinguish linker and loader.
6. Write notes on MSDOS linker.
7. Define Macro.
8. Write the basic Macro processor functions.
9. Define text editor.
10. What is debugger?

PART B — (5 × 16 = 80 marks)

11. (a) Explain SIC machine architecture with suitable example.

Or

- (b) Explain SIC/XE machine architecture in detail.

12. (a) Write in detail about machine dependent assembler features.

Or

(b) Discuss one pass assembler and multi pass assembler in detail.

13. (a) Describe the design of absolute loader and a Bootstrap loader with neat diagram.

Or

(b) Discuss Machine independent loader features in detail.

14. (a) Explain the following Macro processor features in detail:

(i) Concatenation of Macro parameters (6)

(ii) Macro within Macro (5)

(iii) Conditional Macro expansions. (5)

Or

(b) Write in detail about the following:

(i) MASM Macro processor (8)

(ii) ANSI C Macro language (8)

15. (a) Explain the structure of an editor with neat diagram.

Or

(b) Discuss debugging functions and capabilities in detail.